Hunteck

Feature

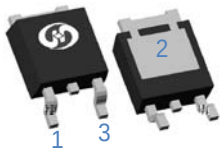
- High Speed Power Switching, Logic Level
- Enhanced Avalanche Ruggedness
- 100% UIS Tested, 100% Rg Tested
- Lead Free, Halogen Free

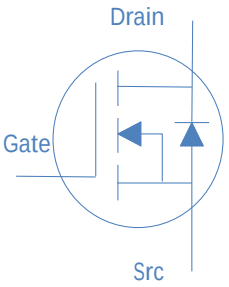
			Ω

Application

- Hard Switching and High Speed Circuit
- DC/DC in Telecoms and Inductrial

TO-252





=25 (unless otherwise specified)

		=25		
		=100		
		=25		
		=25		

	θ		/W
	θ		/W

=25 (unless otherwise

		μ				
		μ				
		=25				μ
		=125				
						Ω
	fs					
		=0V, f=1MHz				Ω

		=15V, f=1MHz				
Reverse Transfer Capacitance						
		Ω				
Turn off Delay Time	d(off)					
	f					

		/dt=100A/μ				

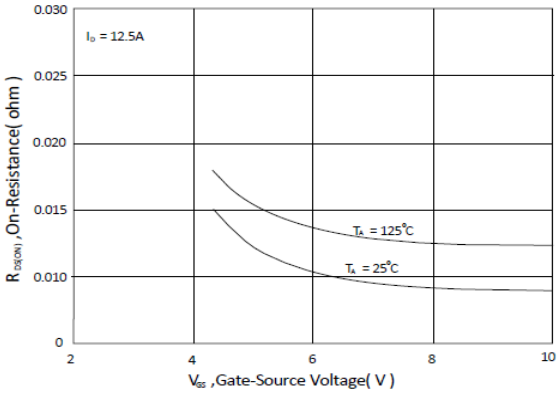


Figure 4. Normalized On-Resistance vs. Junction Temperature

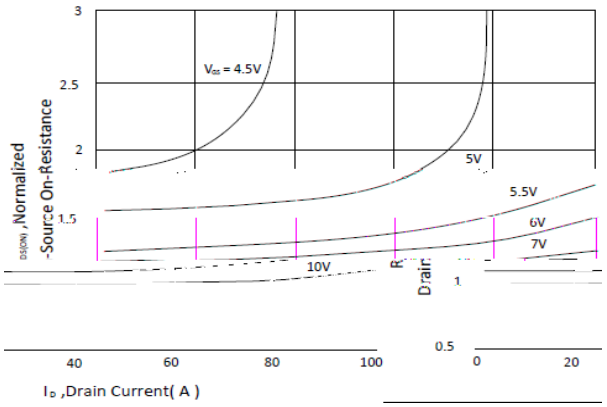


Figure 5. Typical Transfer Characteristics

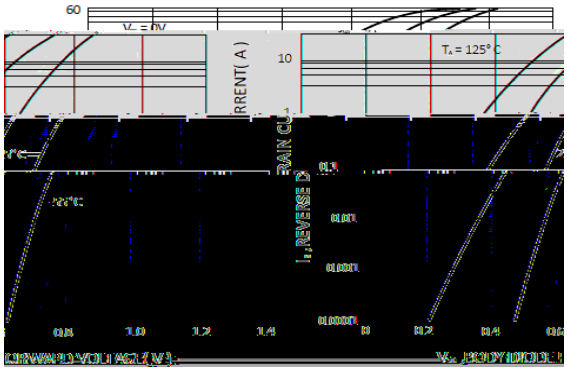
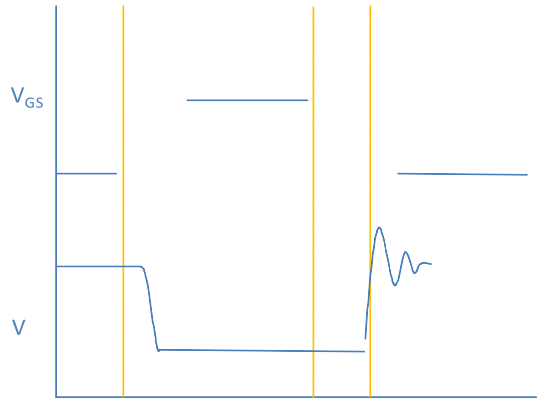
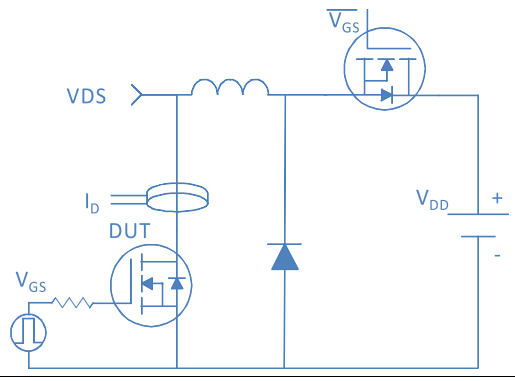
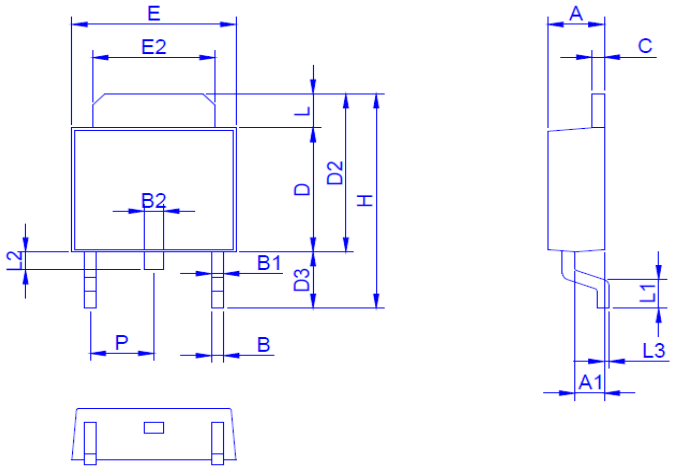


Figure 9. Maximum Safe Operating Area

Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient


V_{GS}
V


V_{GS}
VDS
I_D
DUT
V_{DD}
+
-
V_{GS}



Dimension	A	A1	B	B1	B2	C	D	D2	D3	E	E2	H	L	L1	L2	L3	P
0.60	0.40	5.30	6.70	2.20	6.40	4.80	9.20	0.89	0.90	0.50	0.00	2.10	Min.	2.10	0.95	0.30	0.40
1.00	0.60	6.20	7.30	3.00	6.70	5.45	10.15	1.70	1.65	1.10	0.30	2.50	Max.	2.50	1.30	0.85	0.94